

Evaluation Board EPC9092 Quick Start Guide

100 V Half-Bridge with Gate Drive using EPC2052

March 22, 2019

Version 1.0



DESCRIPTION

The EPC9092 evaluation board is a 100 V maximum device voltage, 5 A maximum output current, half bridge with onboard gate drives, featuring the EPC2052 enhancement mode (eGaN®) field effect transistor (FET). The purpose of this evaluation board is to simplify the evaluation process of the EPC2052 eGaN FET by including all the critical components on a single board that can be easily connected into any existing converter.

The EPC9092 evaluation board is 2" x 2" and contains two EPC2052 eGaN FETs in a half bridge configuration using the Texas Instruments LMG1205 gate driver. The board also contains all critical components and layout for optimal switching performance. There are also various probe points to facilitate simple waveform measurement and efficiency calculation. A block diagram of the circuit is given in figure 1.

For more information on the EPC2052 please refer to the datasheet available from EPC at www.epc-co.com. The datasheet should be read in conjunction with this quick start guide.

QUICK START PROCEDURE

Evaluation board EPC9092 is easy to set up to evaluate the performance of two EPC2052 eGaN FETs. Refer to figure 2 for proper connect and measurement setup and follow the procedure below:

1. With power off, connect the input power supply bus to +V_{IN} (J5, J6) and ground / return to -V_{IN} (J7, J8).
2. With power off, connect the switch node (SW) of the half bridge OUT (J3, J4) to your circuit as required (half bridge configuration). The EPC9092 features an optional buck converter configuration, as shown in figure 2, with unpopulated footprints for an output inductor and output capacitors.
3. With power off, connect the gate drive supply to +V_{DD} (J1, Pin-1) and ground return to -V_{DD} (J1, Pin-2 indicated on the bottom side of the board).
4. With power off, connect the input PWM control signal to PWM (J2, Pin-1) and ground return to any of the ground J2 pins indicated on the bottom side of the board.
5. Turn on the gate drive supply – make sure the supply is between 7.5 V and 12 V range.
6. Turn on the controller / PWM input source.
7. Making sure the supply voltage initially is 0 V, turn on the power and slowly increase the bus voltage to the required value, while observing the switch-node voltage and device temperature for operational limits, (do not exceed the absolute maximum voltage) and probe switching node to see switching operation.
8. Once operational, adjust the PWM control, bus voltage, and load within the operating range and observe the output switching behavior, efficiency and other parameters.
9. For shutdown, please follow steps in reverse.

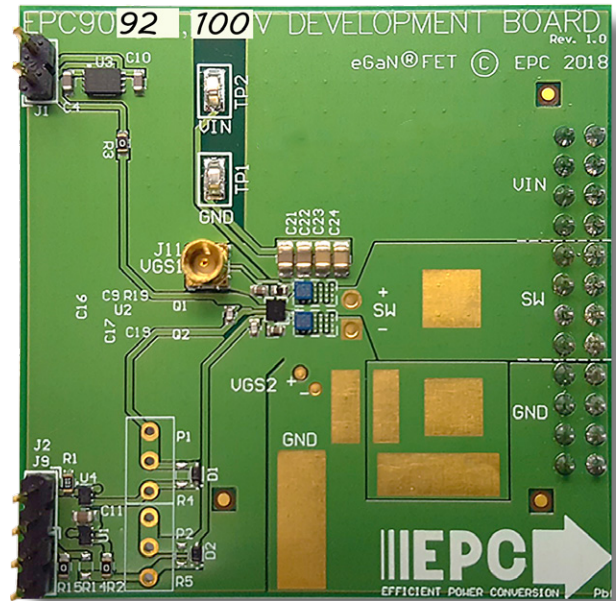
Table 1: Performance Summary (T_A = 25°C) EPC9092

Symbol	Parameter	Conditions	Min	Max	Units
V _{DD}	Gate Drive Input Supply Range		7.5	12	V
V _{IN}	Bus Input Voltage Range ⁽¹⁾			80	V
I _{OUT}	Switch Node Output Current ⁽²⁾			5	A
V _{PWM}	PWM Logic Input Voltage Threshold	Input 'High' Input 'Low'	3.5 0	5.5 1.5	V V
	Minimum 'High' State Input Pulse Width	V _{PWM} rise and fall time < 10ns	50		ns
	Minimum 'Low' State Input Pulse Width ⁽³⁾	V _{PWM} rise and fall time < 10ns	200		ns

(1) Maximum input voltage depends on inductive loading, maximum switch node ringing must be kept under 100 V for EPC2052.

(2) Maximum current depends on die temperature – actual maximum current will be subject to switching frequency, bus voltage and thermal cooling.

(3) Limited by time needed to 'refresh' high side bootstrap supply voltage.



EPC9092 evaluation board

NOTE. When measuring the high frequency content switch node, care must be taken to provide an accurate high speed measurement. An optional two pin header (J10) is included for switch node measurement. It is recommended to install measurement point on backside of board to prevent contamination of the top side components.

For information about measurement techniques, please review the how to GaN series: HTG09- Measurement

<http://epc-co.com/epc/DesignSupport/TrainingVideos/HowtoGaN/>

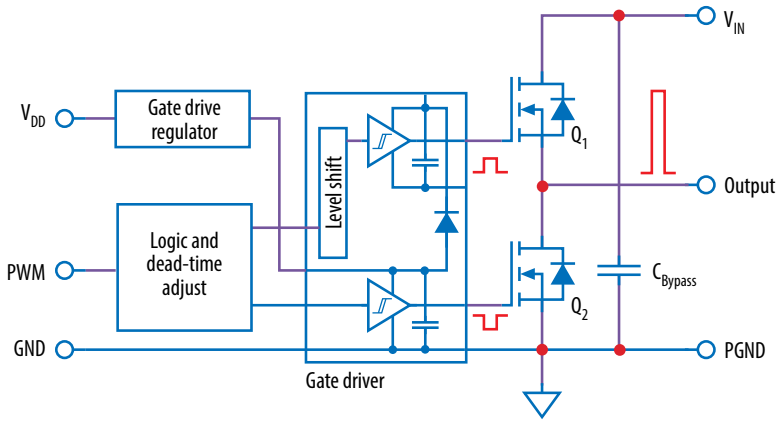


Figure 1: Block diagram of EPC9092 evaluation board

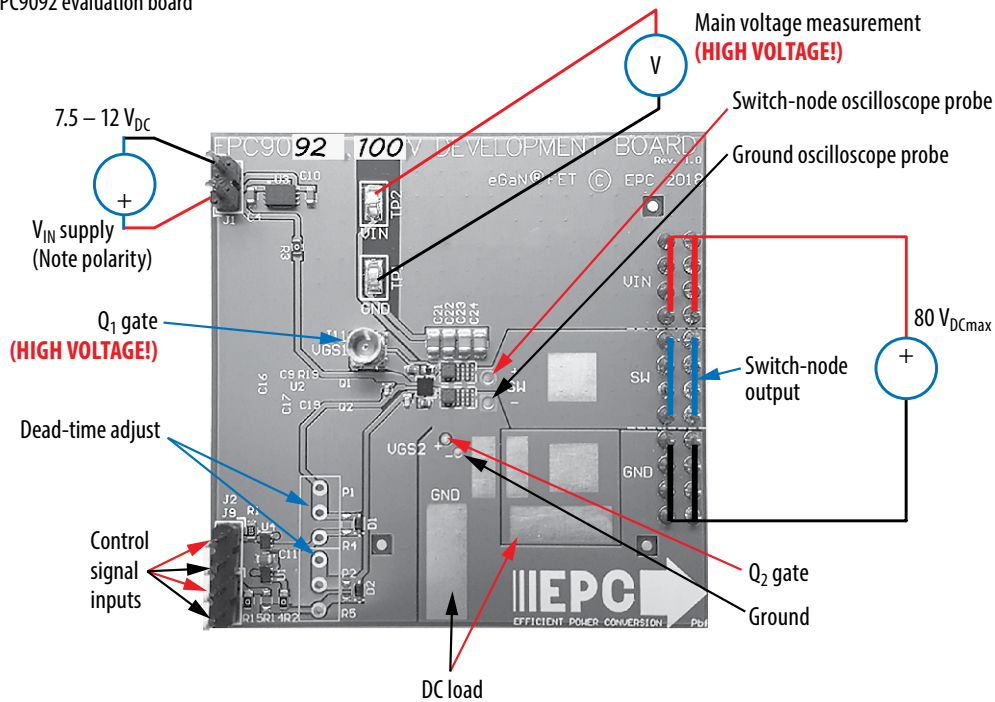


Figure 2: Proper connection and measurement setup

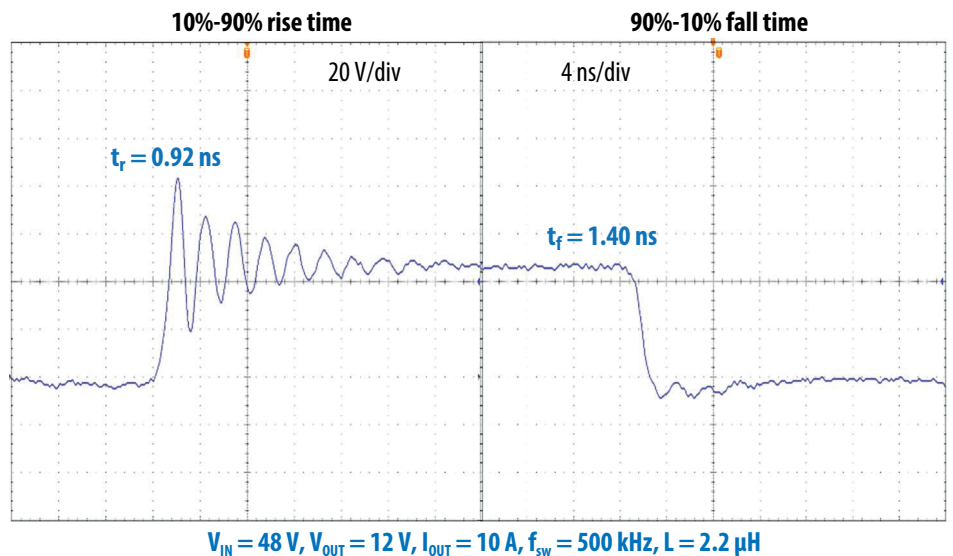


Figure 3: Typical Waveform when operating a Buck Converter

THERMAL CONSIDERATIONS

The EPC9092 evaluation board showcases the EPC2052 eGaN FET. The EPC9092 is intended for bench evaluation with low ambient temperature and convection cooling. The addition of heat-sinking and forced air cooling can significantly increase the current rating of these devices, but care must be taken to not exceed the absolute maximum die temperature of 150° C.

NOTE. The EPC9092 evaluation board does not have any current or thermal protection on board.

For more information regarding the thermal performance of EPC eGaN FETs, please consult:

D. Reusch and J. Glaser, *DC-DC Converter Handbook*, a supplement to *GaN Transistors for Efficient Power Conversion*, First Edition, Power Conversion Publications, 2015.

For support files including schematic, Bill of Materials (BOM), and gerber files please visit the EPC9092 landing page at: <https://epc-co.com/epc/products/demo-boards/epc9092>

For More Information:

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Evaluation Board Notification

The EPC9092 board is intended for product evaluation purposes only and is not intended for commercial use. Replace components on the Evaluation Board only with those parts shown on the parts list (or Bill of Materials) in the Quick Start Guide. Contact an authorized EPC representative with any questions.

This board is intended to be used by certified professionals, in a lab environment, following proper safety procedures. Use at your own risk.

As an evaluation tool, this board is not designed for compliance with the European Union directive on electromagnetic compatibility or any other such directives or regulations. As board builds are at times subject to product availability, it is possible that boards may contain components or assembly materials that are not RoHS compliant. Efficient Power Conversion Corporation (EPC) makes no guarantee that the purchased board is 100% RoHS compliant.

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