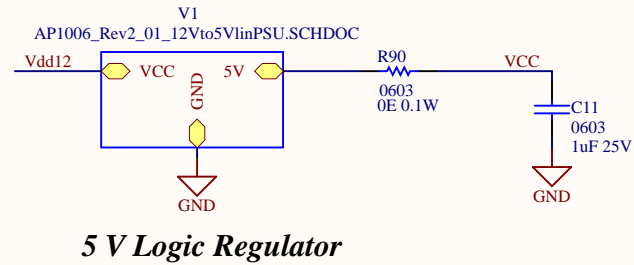
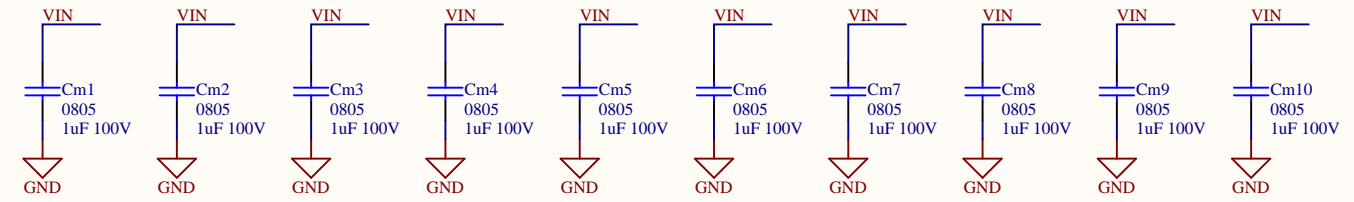


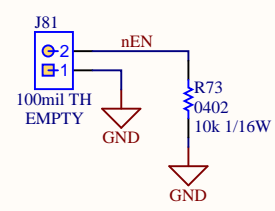
Logic Supply
12VDC



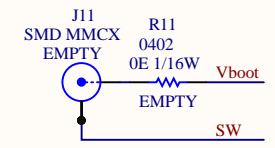
5 V Logic Regulator



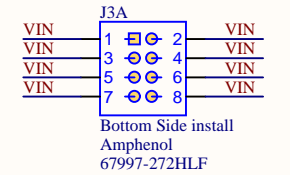
Intermediate Capacitors



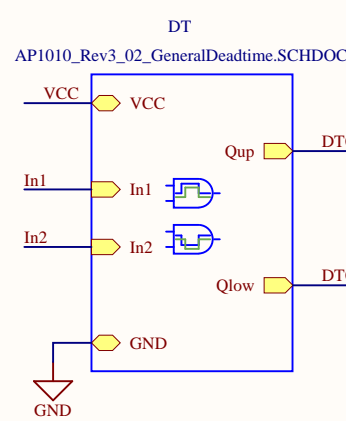
Signal Inputs



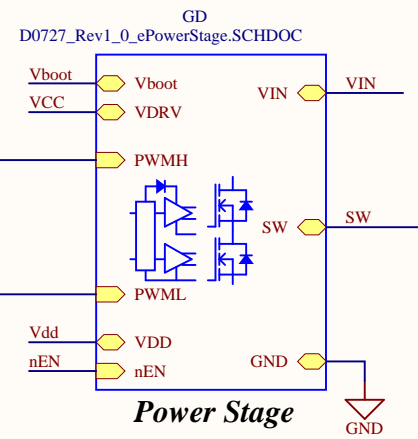
Bootstrap Voltage



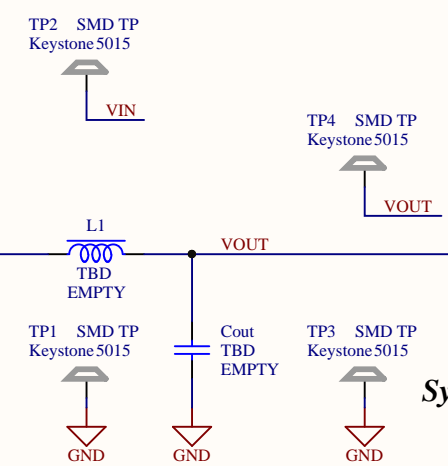
Main Supply Input



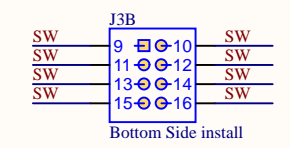
dead-time and buffers



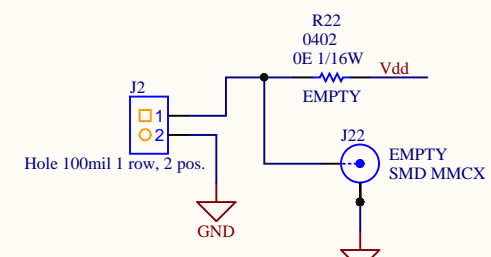
Power Stage



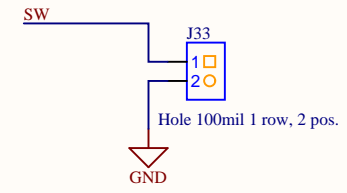
Sync Buck Output



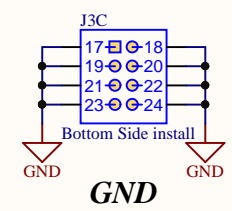
SW Output



Vdd



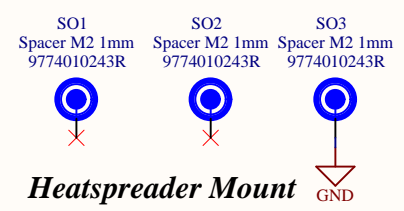
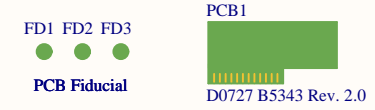
Switch-node



GND



For evaluation only;
not FCC approved for resale

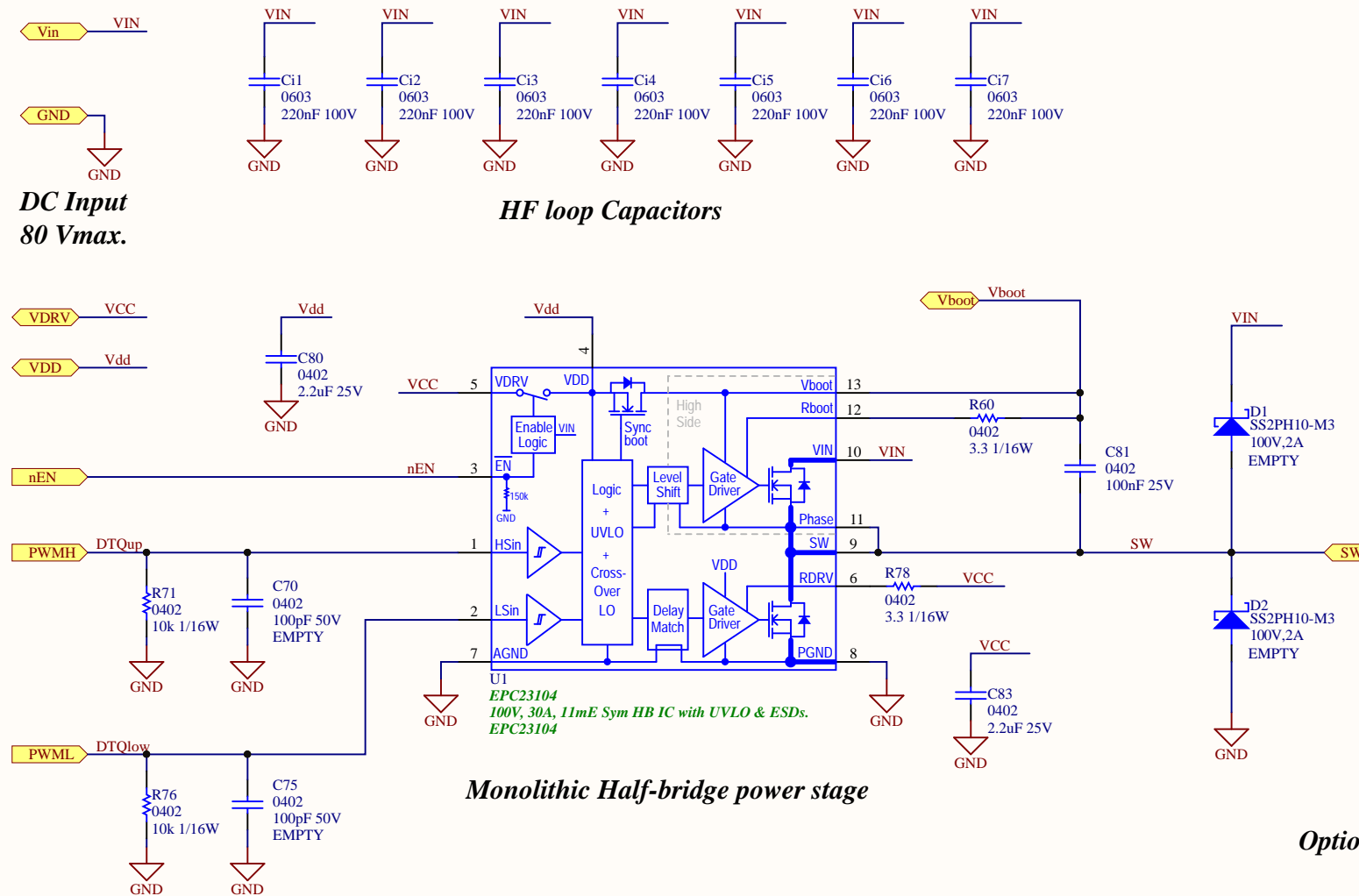


Heatspreader Mount

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Evaluation Board Main Schematic

Title: EPC90152_B5343_Rev2_0		Efficient Power Conversion 909 N. Pacific Coast Hwy, Ste. 23 El Segundo, CA 90245 United States www.epc-co.com	
Size: A	1	Revision: 1	
Date: 12/23/2022	Sheet 1 of 4		
File: D0727_B5343_Rev2_0.SCHDOC			






DC Input
80 Vmax.

HF loop Capacitors

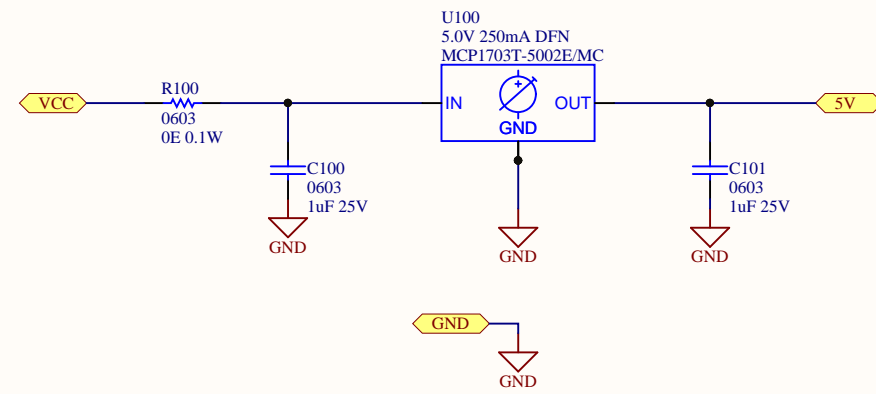
Monolithic Half-bridge power stage

Optional Diodes

EPC23104

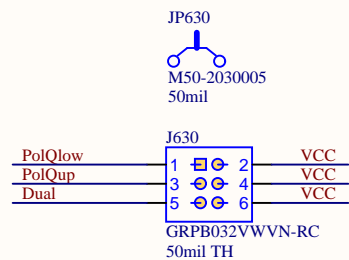
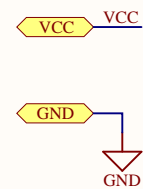
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Size: A	2	Revision: 1	
Date: 12/23/2022	Sheet 2 of 4		
File: D0727_Rev1_0_ePowerStage.SCHDOC			

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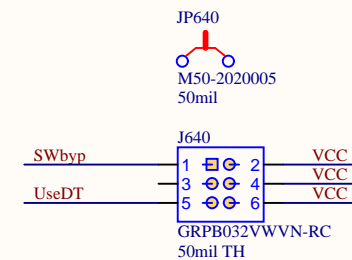
12 V to 5 V LDO power supply

Title AP1006 Rev. 2.0		Efficient Power Conversion 909 N. Pacific Coast Hwy, Ste. 23 El Segundo, CA 90245 United States www.epc-co.com		
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Date: 9/20/2022		Sheet 3 of 4		
File: AP1006_Rev2_01_12Vto5VinPSU.SCHDOC				



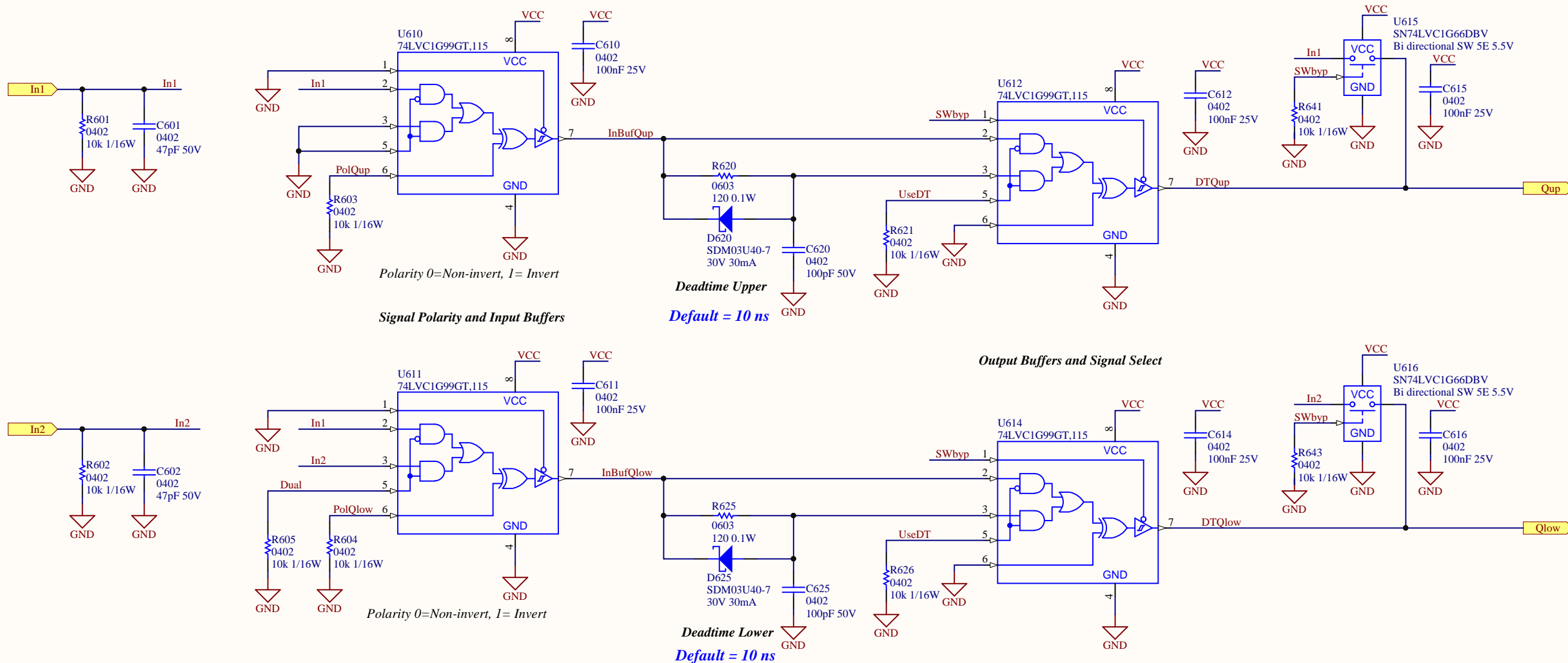
Buck Single Signal
Boost Single Signal
Dual Signal

Dual/Single PWM, Buck, and Boost Mode Selector



Full Bypass
DT Bypass
No Bypass

Bypass Mode Select



General Dead-time with Polarity Changer and Bypass

Title AP1010 Rev. 3.0		Efficient Power Conversion 909 N. Pacific Coast Hwy, Ste. 23 El Segundo, CA 90245 United States www.epc-co.com	
Size: A	4	Revision: 1	
Date: 9/20/2022	Sheet 4 of 4		
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