Efficient Power Conversion

5 V Logic Regulator

Intermediate Capacitors

Main Supply Input

High Voltage

Sync Buck Output

SW Output

Upper Gate

Gate Driver

dead-time and buffers

Power Stage

Lower Gate

Switch-node

Heat spreader Mount
DC Input
80 V max.

HF loop Capacitors

Power Stage
Optional Diodes

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Sync Boot = Install R60 and D77, remove R77
No Sync Boot = Install R77, remove R60 and D77
Default = No Sync Boot

Synchronous Bootstrap Power Supply

Gate Driver
Efficient Power Conversion

Dual/Single PWM, Buck, and Boost Mode Selector

Signal Polarity and Input Buffers

Polarity 0=Non-invert, 1= Invert

Deadtime Upper
Default = 10 ns

Output Buffers and Signal Select

Polarity 0=Non-invert, 1= Invert

Deadtime Lower
Default = 10 ns

General Dead-time with Polarity Changer and Bypass

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