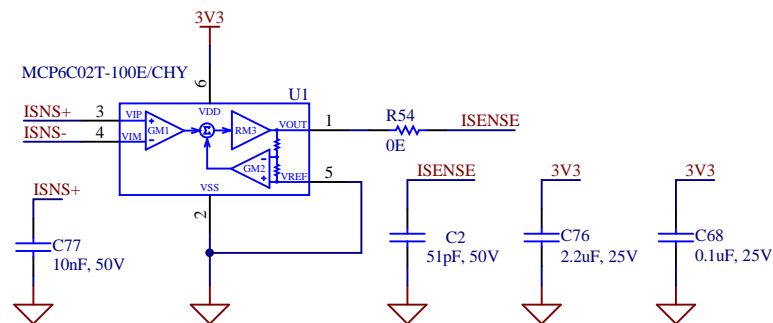
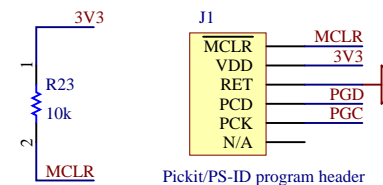


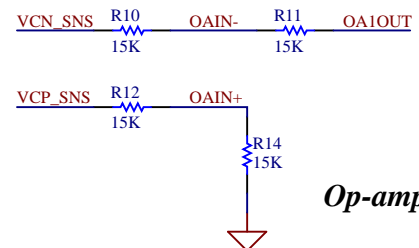
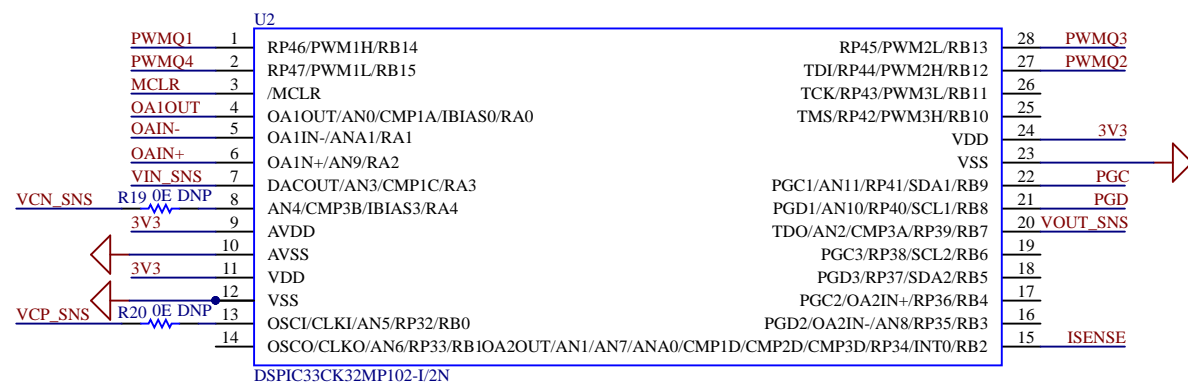
Voltage sense



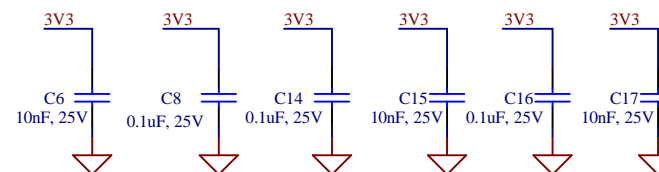
Current sense



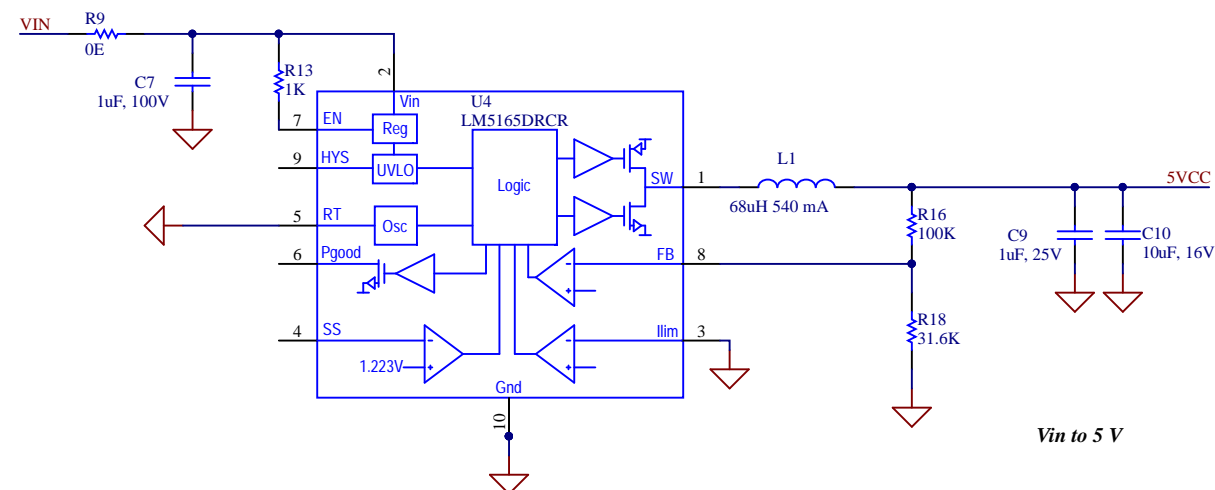
Programming and Communications



Op-amp



Decoupling Caps for the PIC



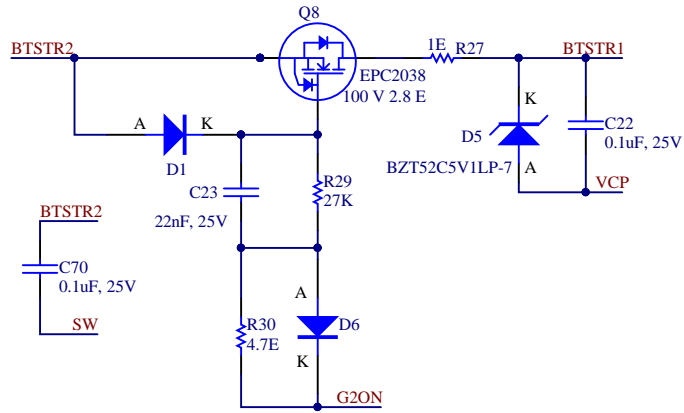
Vin to 5 V

5 V to 3.3 V

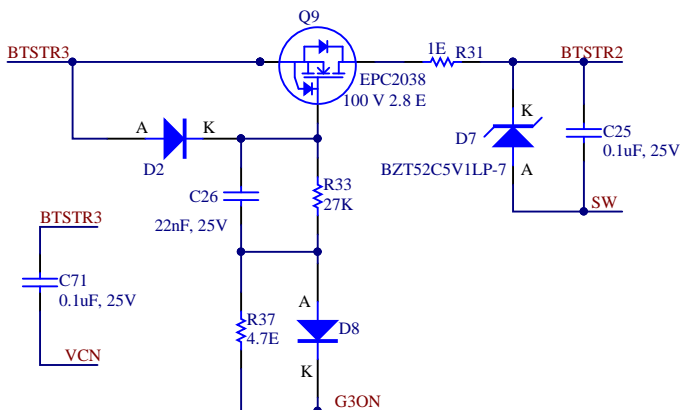
Housekeeping power supply

© Copyright EPC 2020

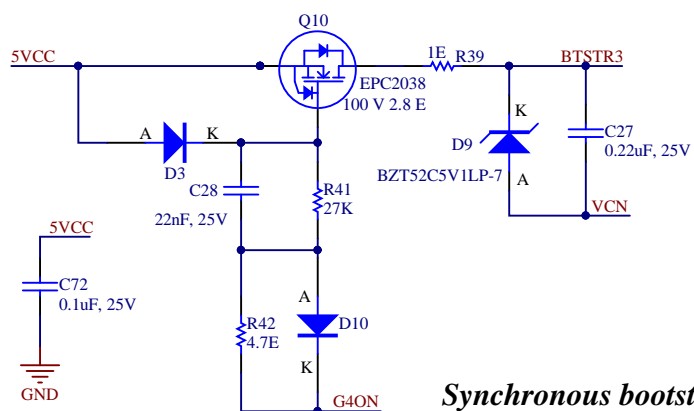
A



B

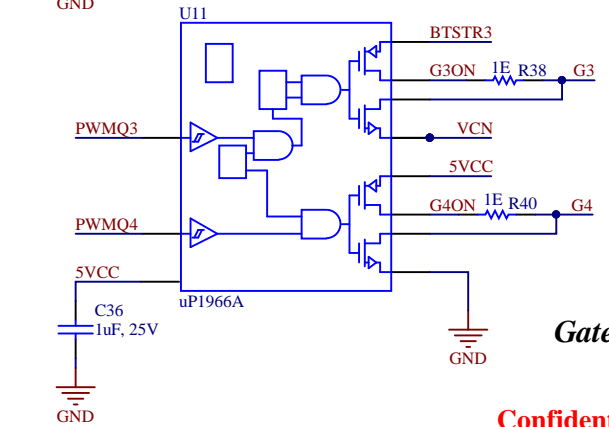
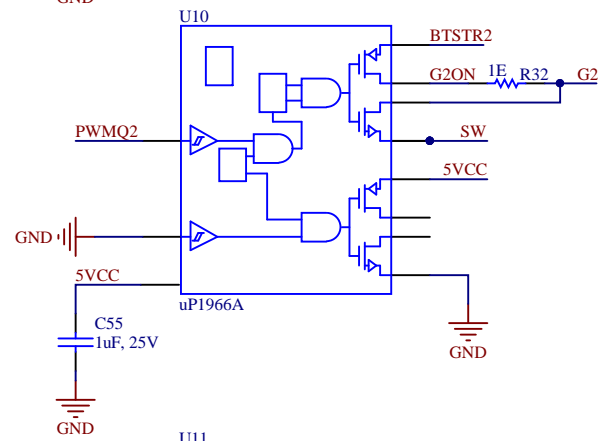
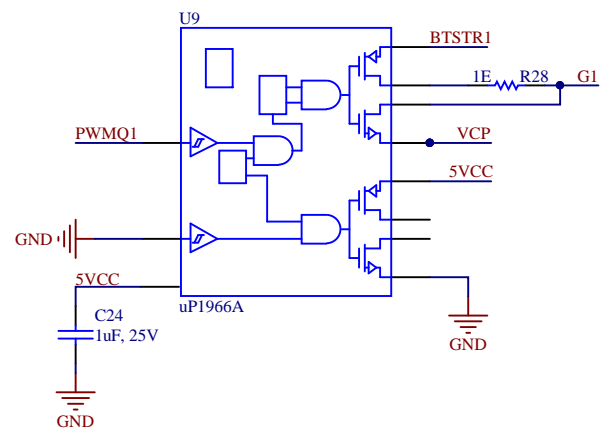


C




D

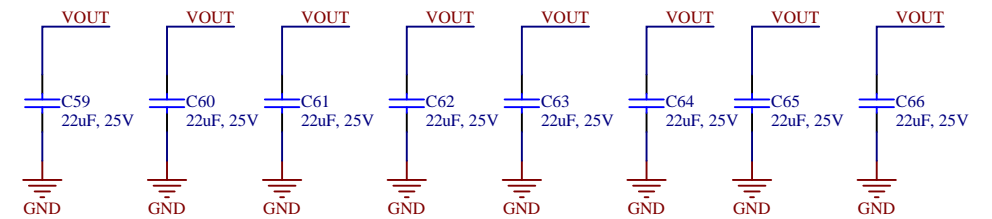
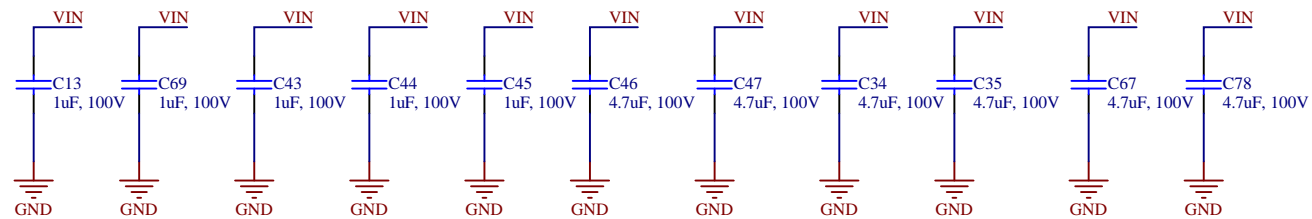
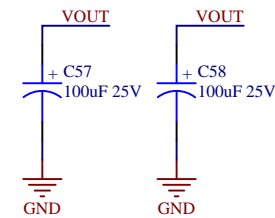
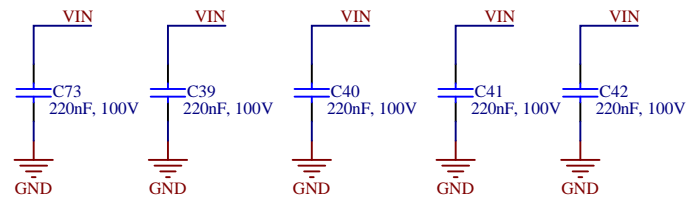
Synchronous bootstrap



Gate driver circuit

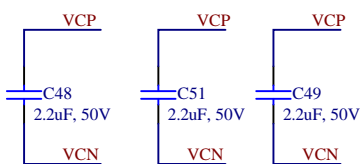
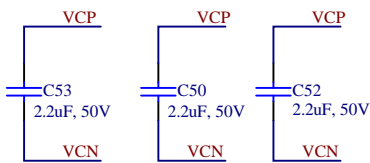
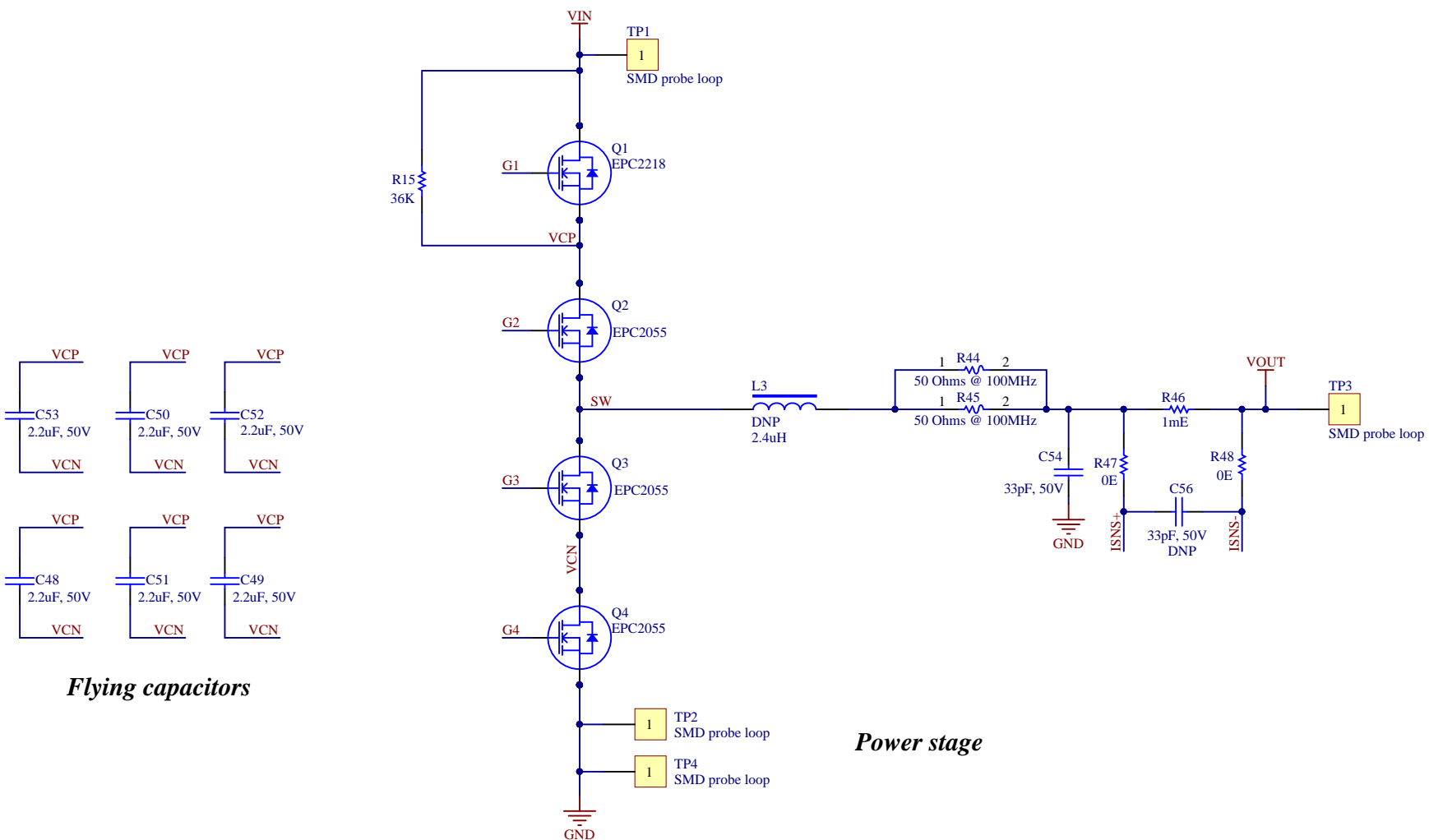
Confidential © Copyright EPC 2020

Title EPC9148_driverStage_ver1		* 909 N. Pacific Coast Hwy, Ste. 23 El Segundo, CA, U.S.A.
Size: A	Revision: *	* * *
Date: 1/24/2020	Sheet * of *	* * *
File: EPC9148_B5222_driverStage_Rev1_0.SchDoc		



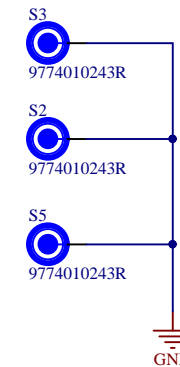
Input capacitors

Output capacitors



Flying capacitors

Power stage



Heatspreader Standoffs



© Copyright EPC 2020

Title EPC9148_ver1_powerStage		* 909 N. Pacific Coast Hwy, Ste. 23 El Segundo, CA, U.S.A.
Size: B	Revision: *	* * *
Date: 3/16/2020	Sheet * of *	* * *
File: EPC9148_B5222_powerStage_Rev1_0.SchDoc		

