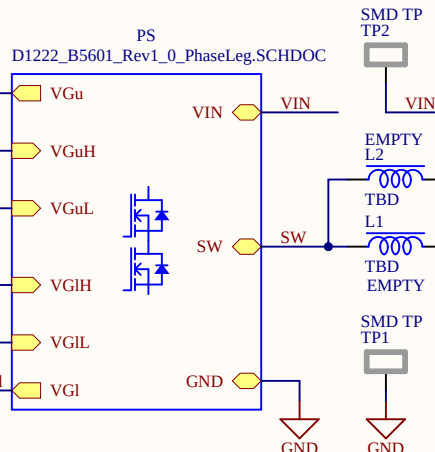
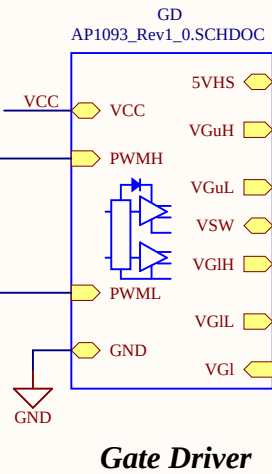
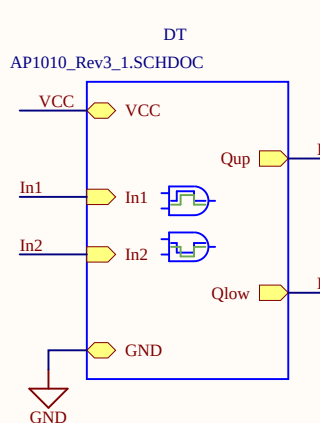
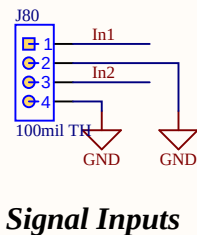
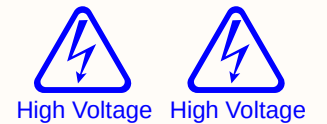
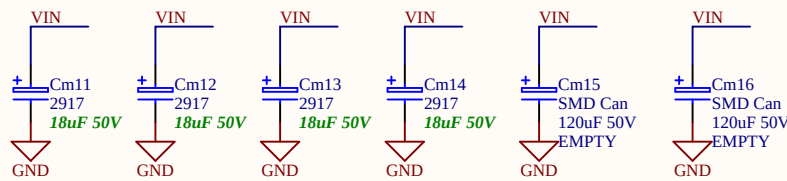
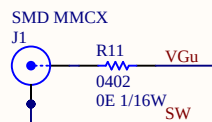
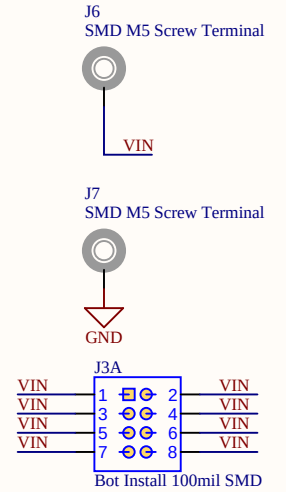
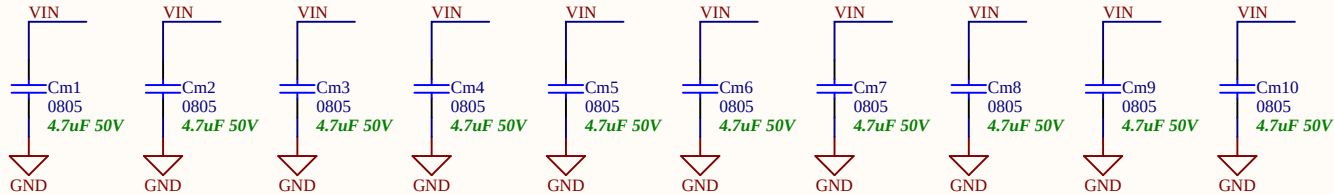
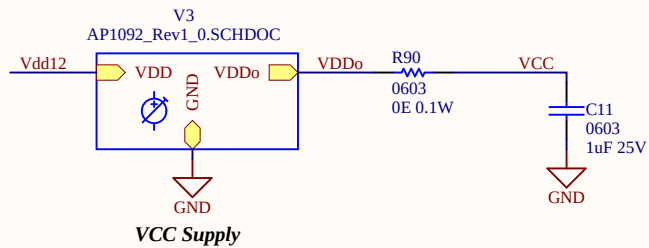
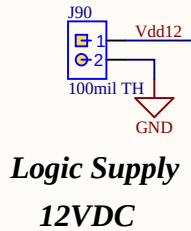
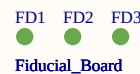


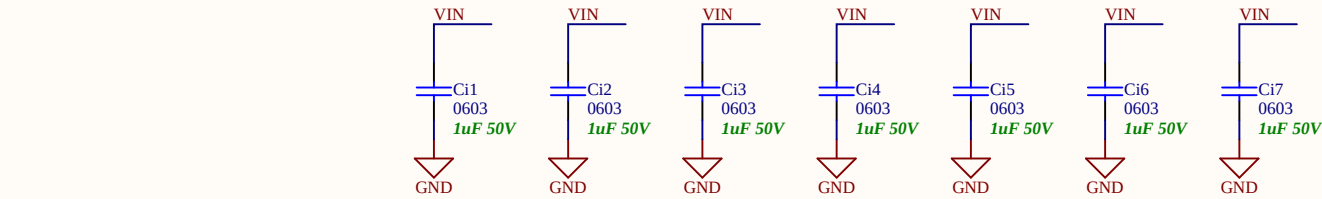
Serial# yyww-nnn



For evaluation only;
not FCC approved for resale

Title: Main Schematic		© EPC 2026	
Design #: EPC90186	PCB #: B5601	Efficient Power Conversion	
Revision 1.0	Revision: 1.0	909 Pacific Coast Hwy. Ste 230	
Date: 10/12/2026	Sheet 1 of 5	El Segundo, CA 90245	
File: D1222_B5601_Rev1_0.SCHDOC		U.S.A.	
		www.epc-co.com	



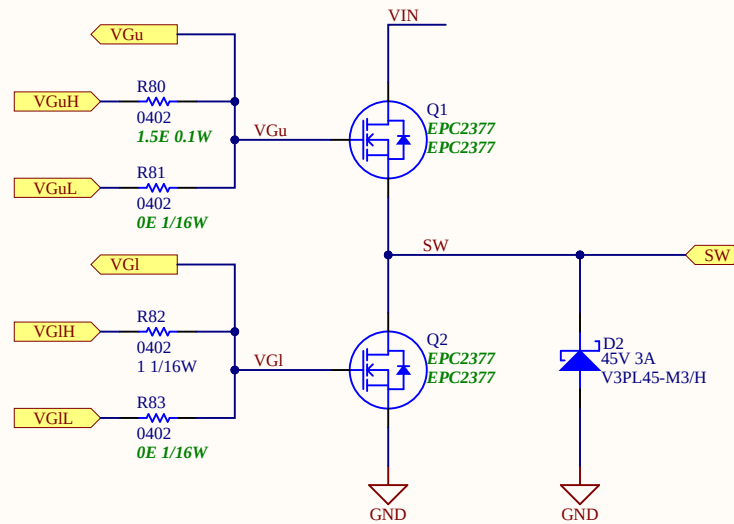
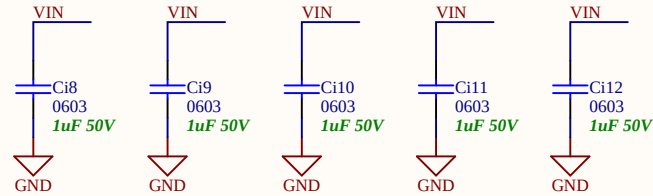


HF loop Capacitors

Vin VIN

GND
GND

**DC Input
40 Vmax.**



Power Stage

Optional Diodes

Title: **HB Sub Schematic - Phase Leg**

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Design #: **D1222 Phase Leg**

Revision **1.0** P/N#: **EPC2377**

Date: 10/12/2026

Sheet **2** of **5**

File: D1222_B5601_Rev1_0_PhaseLeg.SCHDOC

Efficient Power Conversion

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A

A

B

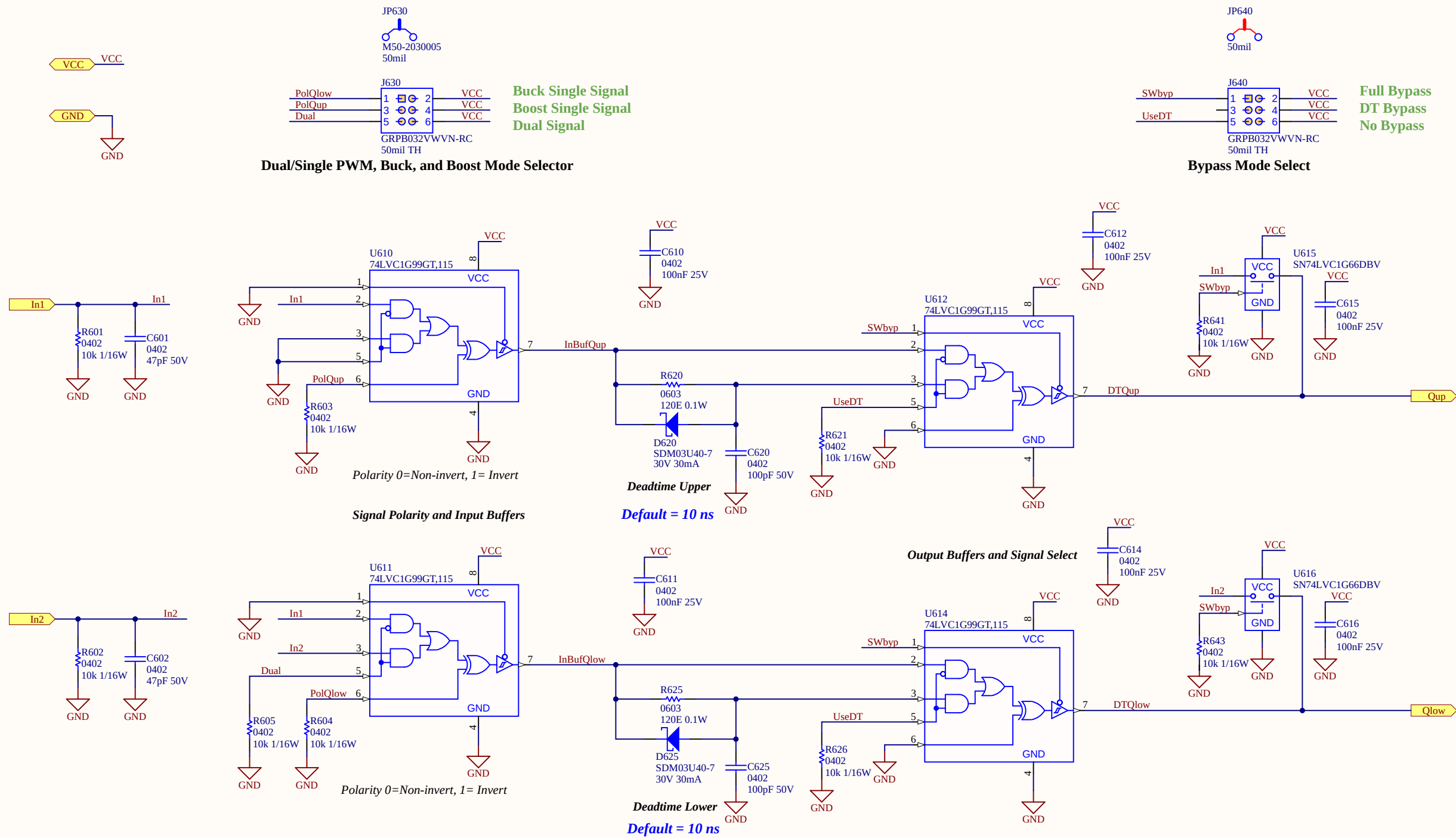
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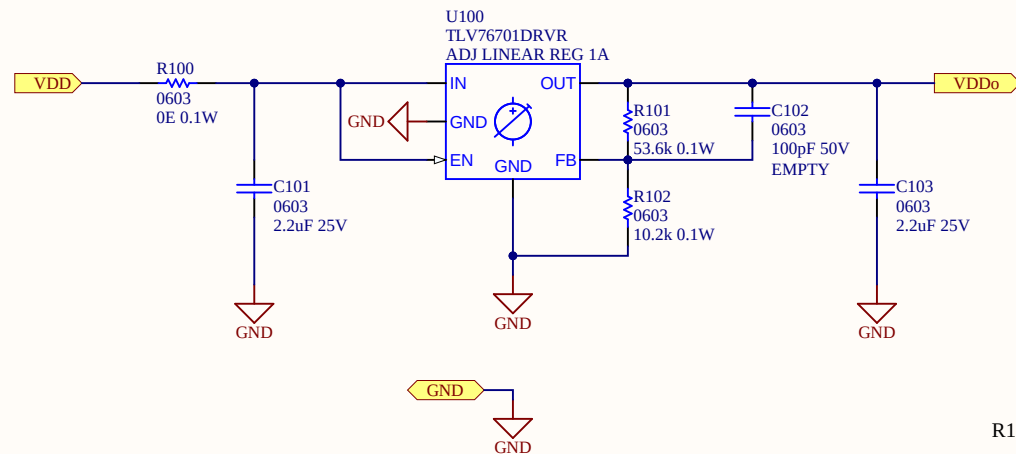
C

C

D

D





R101 R102 VDDo		
53.6k	10.2k	5V
54.9k	10.2k	5.1V
56.2k	10.2k	5.2V
54.9k	9.76k	5.3V
56.2k	10.2k	5.4V
60k	10.2k	5.5V

Title: 16 V to 5 V, 1 A Linear Regulator

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Design #: AP1092

Revision 1.0

Date: 10/12/2026

File: AP1092_Rev1_0.SCHDOC

Sheet 4 of 5

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